

INFORMATION DISCLOSURE STATEMENT BY APPLICANTS LIST OF REFERENCES	Attorney Docket No. 2885/87	Serial No. 10/501,903
	Applicant(s) Martin Vorbach et al.	
	Filing Date March 1, 2005	Group Art Unit 2193

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	3,564,506	February 16, 1971	Bee et al.			
	4,873,666	October 10, 1989	Lefebvre et al.			
	6,321,298	November 20, 2001	Hubis			
	4,939,641	July 3, 1990	Schwartz et al.			
	5,119,290	June 2, 1992	Loo et al.			
	5,435,000	July 18, 1995	Boothroyd et al.			
	5,584,013	December 10, 1996	Cheong et al.			
	5,717,890	February 10, 1998	Ichida et al.			
	5,915,099	June 22, 1999	Takata et al.			
	2003/0056062	March 20, 2003	Prabhu			
	2006/0036988	February 16, 2006	Allen et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Altera, "Implementing High-Speed Search Applications with Altera CAM," July 2001, Ver. 2.1, Application Note 119, 50 pages.
	Bolsens, Ivo (CTO Xilinx), "FPGA, a history of interconnect," Xilinx slide presentation, posted on the internet October 30, 2008 at http://www.docstoc.com/docs/2198008/FPGA-a-history-of-interconnect , 32 pages.
EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	